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Product Specification	

Product Specification

Model Name : AML63910802340-01

Description : 6.39" FHD+ AMOLED

Version : 01

Customer : Common Customers

☐Approved for Preliminary Specification

☐Approved for Final Specification

☒Approved for Final Specification & Sample

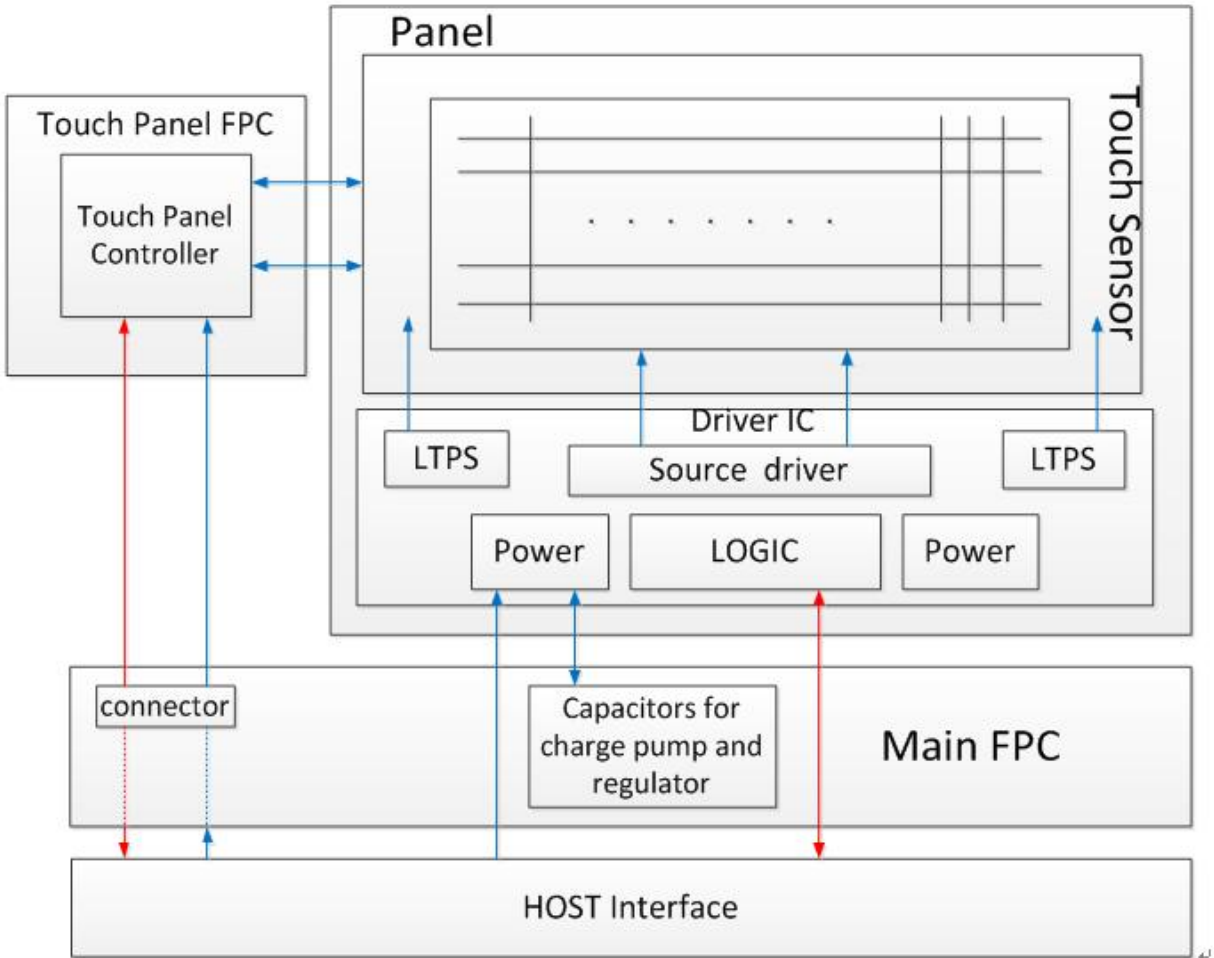
Prepared	Checked	Approved

Customer's Approval

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45	GND	GND	GND
46	TSP_INT	I/O	State change interrupt for TSP
47	TSP_SCL	I	Serial interface Clock for TSP
48	TSP_SDA	I/O	Serial interface Data for TSP
49	TSP_RESET	I	Active low , RESET the Touch IC
50	GND	GND	GND

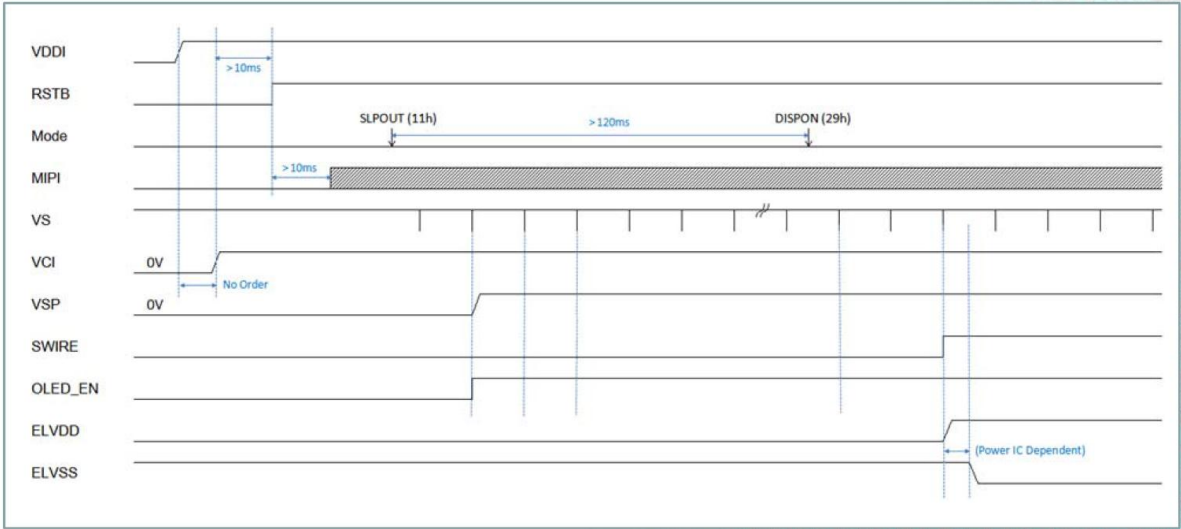
5.2.2 Display Module Block Diagram



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5.3 Recommended Operating Sequence

5.3.1 Power on sequence



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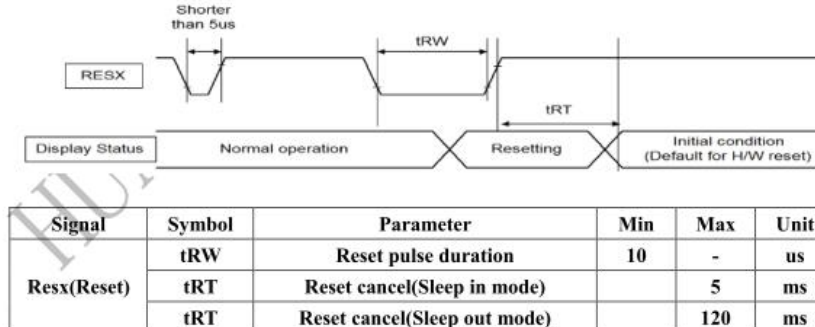
5.4.4 Timing Parameters

Symbol	Description	Min	Typ	Max	Unit
T_{REOT}	30%-85% rise time and fall time	-	-	35	ns
$T_{CLK-MISS}$	Timeout for receiver to detect absence of Clock transitions and disable the Clock Lane HS-RX.	-	-	60	ns
$T_{CLK-POST}^{*1}$	Time that the transmitter continues to send HS clock after the last associated Data Lane has transitioned to LP Mode. Interval is defined as the period from the end of $T_{HS-TRAIL}$ to the beginning of $T_{CLK-TRAIL}$.	$60ns + 52*UI$ (For DCS)	-	-	ns
$T_{CLK-PRE}$	Time that the HS clock shall be driven by the transmitter prior to any associated Data Lane beginning the transition from LP to HS mode.	8	-	-	ns
$T_{CLK-SETTLE}$	Time interval during which the HS receiver shall ignore any Clock Lane HS transitions, starting from the beginning of $T_{CLK-PRE}$.	95	-	300	ns
$T_{CLK-TERM-EN}$	Time for the Clock Lane receiver to enable the HS line termination, starting from the time point when	Time for Dn to reach VTERM-EN		38	ns

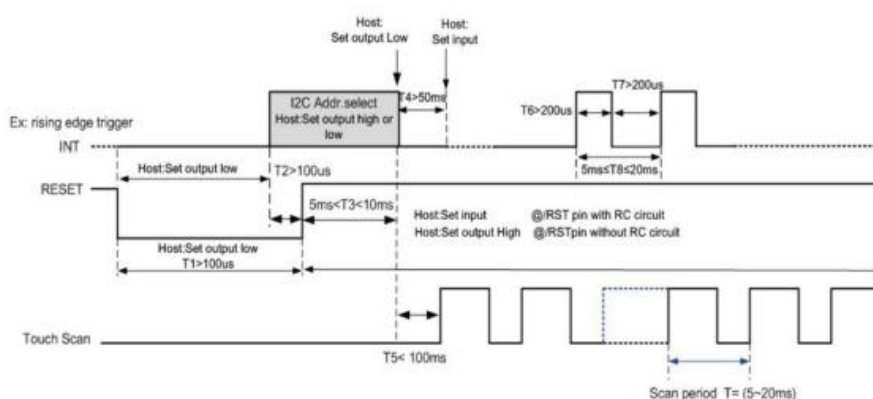
	between Master and Slave side				
T_{TA-GET}	Time to drive LP-00 by new TX	$5 * TLPX$	$5 * TLPX$	$5 * TLPX$	ns
T_{TA-GO}	Time to drive LP-00 after Turnaround Request	$4 * TLPX$	$4 * TLPX$	$4 * TLPX$	ns
$T_{TA-SURE}$	Time-out before new TX side starts driving	$TLPX$	-	$2 * TLPX$	ns

5.4.5 Reset Timing Sequence Requirement

Display panel reset timing:



TP reset timing:



Signal	Symbol	Parameter	Min	Max	Unit
TSP RESET	T1	TSP Reset pulse duration	100	-	us

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6 Touch Specifications

#	Pin_name	I/O	Description
1	TEST1	TEST	TEST
2	TEST2	TEST	TEST
3	GND1	GND	GND
4	TP_RST	I	Active low , RESET the Touch IC
5	TP_SDA	I/O	Serial interface Data for TSP
6	TP_SCL	I	Serial interface Clock for TSP
7	TP_INT	I/O	State change interrupt for TSP
8	NC1	NC	NC
9	IOVCC1	Power	Digital Power for TSP
10	IOVCC2	Power	Digital Power for TSP
11	GND2	GND	GND
12	VDD1	Power	Analog Power for TSP
13	VDD2	Power	Analog Power for TSP
14	GND3	GND	GND
15	TEST3	TEST	TEST
16	TEST4	TEST	TEST

7 Electro-Optical Specification

Item		Symbol	Conditions	Min.	Typ.	Max.	Unit	Remark
Brightness			Full White	387	430	473	cd/m2	Note 1
Brightness(HBM)			HBM Mode	600			cd/m2	
Brightness Uniformity				60	70	-	%	Note 2
Contrast Ratio		CR		100,000	-	-		Based on CS-2000 Note 3
CIE Chromaticity	White	X	Normal to surface (CIE 1931)	0.279	0.295	0.319	-	Ref.
		Y		0.295	0.315	0.335	-	Ref.
	Red	X		0.658	0.687	0.718	-	Ref.

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8 Reliability

8.1 Environmental Test

No	Item	Conditions	Note
1	High Temperature Operation 5pcs	$70 \pm 2^{\circ}\text{C}$ / 120 hours	Base on Full-MDL
2	Low Temperature Operation 5pcs	$-20 \pm 2^{\circ}\text{C}$ / 120 hours	Base on Full-MDL
3	High Temperature and Humidity Operation 5pcs	60°C / 93% RH 120 hrs	Base on Full-MDL
4	High Temperature Storage 5pcs	$85^{\circ}\text{C} \pm 2^{\circ}\text{C}$ / 120hrs	Base on Full-MDL
5	Low Temperature Storage 5pcs	$-40 \pm 2^{\circ}\text{C}$ / 120 hours	Base on Full-MDL
6	Thermal Shock Storage 5pcs	-30°C (30min/cycle) / $+80^{\circ}\text{C}$ (30min/cycle) 50cycle (2hr/cycle)	Base on Full-MDL

Note 9: ESD 测试

No	Item	Conditions	Note
1	Air Discharge (Center, 20 times)	$\pm 4\text{kV}$, (FOG level)	After testing - Hard defect should not happen.

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2	Y 轴向	30	面 4 放置工作台上 ,装 夹方式是压紧
3	X 轴向	30	面 6 放置工作台上 ,装 夹方式是压紧

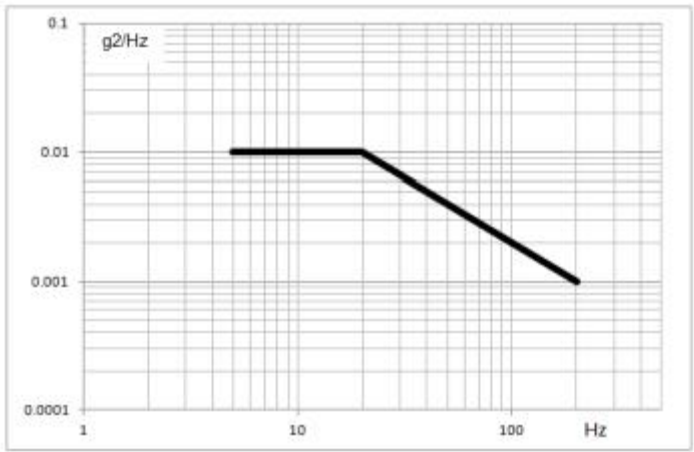
表 3 随机振动试验参数表

若产品有明确的放置面 ,则要求放置面始终放在振台面上。即 Y 轴向和 X 轴向测试放在振动台的水平滑台上。

频率 (HZ)	PSD (功率谱密度 , g ² /Hz)
5	0.01
20	0.01
200	0.001
均方根加速度 (Grms)	0.781

表 4 随机振动断点设置表

根据上表设置的随机振动频谱曲线 (PSD VS Hz) 应符合下图 :



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10 Outline Dimension Drawing

